



# CSC COMPUTER EDUCATION (P) LTD.

GSTIN : 33AABCC2161J1Z1

Corporate Office : #151/3, South West Boag Road, T.Nagar, Chennai - 600 017.

Telephone : 044 - 45901234

Website: www.iteducationjobs.com E-mail : tnagar.csc@gmail.com

CIN No: U72200TN1999PTC042240

DATE: 25-06-2024

## FROM CSC PLACEMENT CELL

Dear Sir / Madam,

Greetings Panacea Medical Technologies Pvt Ltd.....!!!

**Company profile:** On the overview of the company, Panacea Medical Technologies Pvt. Ltd. emerged as India's leading inventor and implementer of latest and best leading technologies enabled to come up with pre-eminent added value and advanced medical equipment's for Radiotherapy and Radiology.

We have Job openings for the below positions & JD is mentioned below for your reference:

**Position: Software Engineer**

**Qualification: MCA or BE in Computer Science/Electronics & Communication**

**Location – Malur, Kolar District**

**Experience: 2-5 Years**

**No of Positions: 5**

- Strong programming skills in Visual C# – Mandatory
- Database development using SQL Server
- GUI Programming & 3rd Party Custom Controls
- Knowledge of SDLC, Entity framework, WPF & Network Application Development
- C# [.Net 4.6], WPF, MVVM, WCF, Data Structure, Algorithms, Multi-threading,
- Design patterns, Sql, XML, Xunit, NUnit, Visual Studio, Prism
- Good understanding of OOPS concepts and socket programming.

**Position: FPGA and VHDL/Verilog Engineer**

**Qualification:** B.E / B.Tech / ME / MTech/Ph.D. in Electronics & Communication, M Tech in VLSI Design

**Work Location:** Malur, 563130, Kolar (District)

**Detailed Job Description / Role**



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Primary Job responsibility is to design & develop FPGA based embedded hardware. The tasks will include selection of FPGA ICs, design and documentation of VHDL/Verilog, board bring up, unit testing, integration with overall machine and associated integration and system level testing. Adherence to defined engineering process is also required as part of the role.

### **Technical Skills required / Tool Knowledge**

- Expertise in designing and implementing FPGA designs in Verilog for high end FPGA devices, simulation, synthesis and Timing Analysis
- Experience in defining and performing FPGA module level integration and test plans
- Good understanding of FPGA architecture and tool flow
- Strong fundamentals, experience in analyzing and troubleshooting
- Experience in embedded systems, architecture and programming

### **Interview Process**

1. Written Test
2. 1st Round Technical Round
3. Assignment
4. Final Technical Round
5. HR Round

### **CTC & Benefits**

1. B.E / B.Tech / MCA / MSc - During Training period for 0 to 3 months candidate will be getting Rs.20000/- fixed stipend per month, after successfully completion of the training, candidate CTC will be revise to Rs.4,00,000/- Annual CTC (Including All the Benefits) for, (Gross Salary Rs. 27,000/- , Take Home Salary Rs.25,000/-)
2. M.Tech / M.E / M.S - During Training period for 0 to 3 months candidate will be getting Rs.23000/- fixed stipend per month, after successfully completion of the training, candidate CTC will be revise to Rs.5,00,000/- Annual CTC (Including All the Benefits) for, (Gross Salary Rs. 32,000/- , Take Home Salary Rs.30,000/-)
3. PF, Gratuity, Bonus, Incentives, Medical Insurance Coverage
4. Free Transportation Facility will be provide to nearest local City
5. Subsidise Canteen Facility
6. Employee Stock Options benefits
7. Service Award Etc.



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## **We would like you to arrange the mentioned arrangements for the recruitment procedures.**

1. Please circulate attached our company profile to your students.
2. Candidates Marks should be Minimum 70% in all the academics without any backlogs in any semester
3. Candidate should be ready to relocate to Malur, 563130, Kolar (District).
4. Once joined the organization candidate should work for minimum period of 24 months from the date of joining. If the candidate is leaving this organization for any reason prior to the minimum working period of 24 months, than the candidate should be liable to re-pay the training & operational expenses of Rs.75000/- to the company."
5. Please note that applicants who do not meet these requirements will not be allowed to take the test.

If your students have any questions or wish to contact me regarding these positions, I may be reached at 9590324262

We are looking forward to a mutually beneficial and long lasting relationship with your esteemed organization.

Thanks & Regards,

Girisha K

Sr. Manager - HR, IR & Admin

Ph No: +91-9590324262

**K.R.GOWRI SHANKAR**

Head - Placement